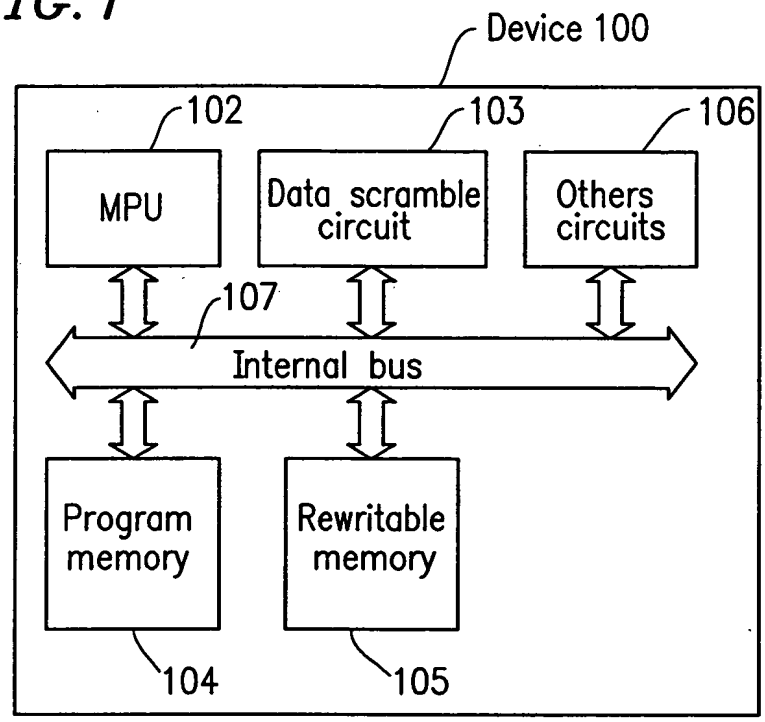
FIG. 1


Device 100
102
103
106
MPU
Data scramble circuit
Others circuits
107
Internal bus
Program memory
Rewritable memory
104
105

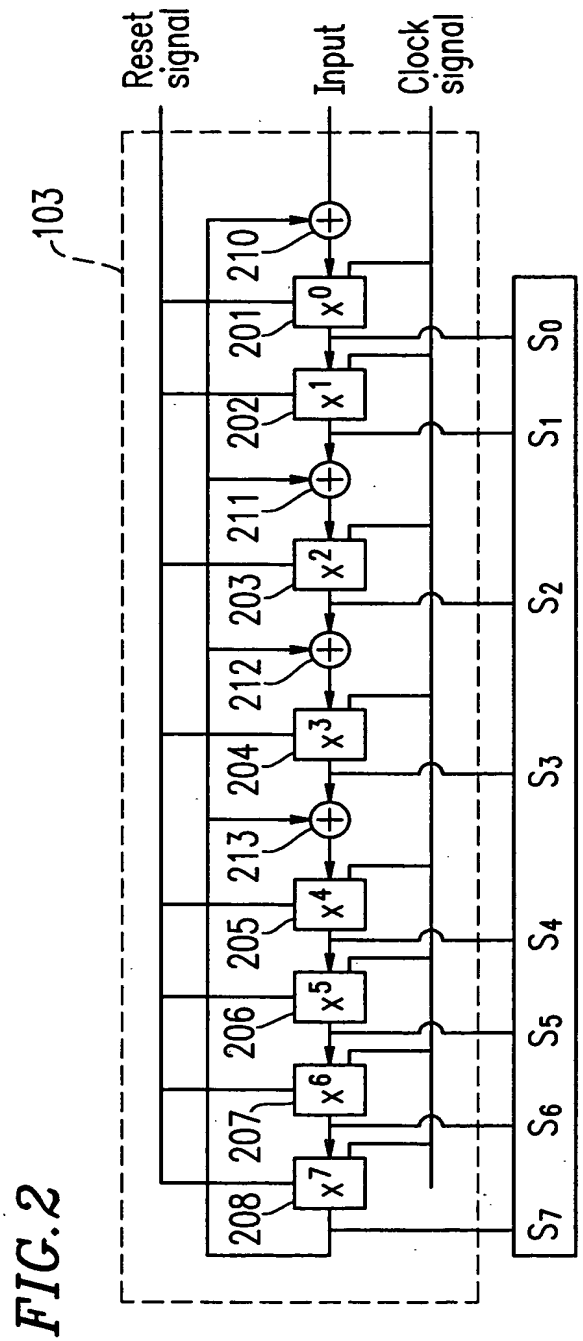

Reset signal
Input
Clock signal
103
210
201
202
211
203
212
204
213
205
206
207
208
x0
x1
x2
x3
x4
x5
x6
x7
S0
S1
S2
S3
S4
S5
S6
S7

FIG.2

FIG. 3

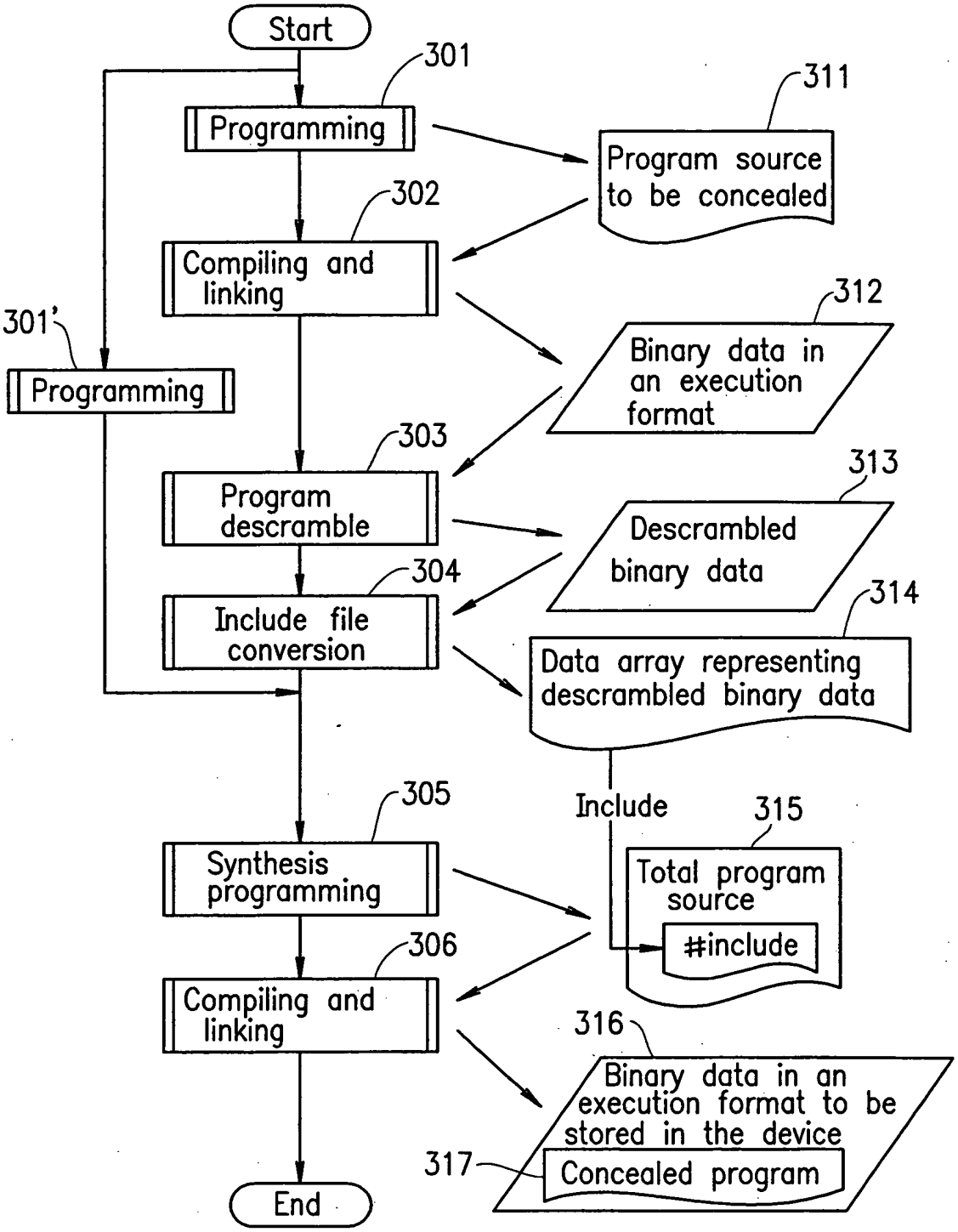

Start
301
Programming
311
Program source to be concealed
302
Compiling and linking
312
301'
Programming
Binary data in an execution format
303
Program descramble
313
Descrambled binary data
304
Include file conversion
314
Data array representing descrambled binary data
Include
315
305
Synthesis programming
Total program source
#include
306
Compiling and linking
316
Binary data in an execution format to be stored in the device
317
Concealed program
End

FIG. 4

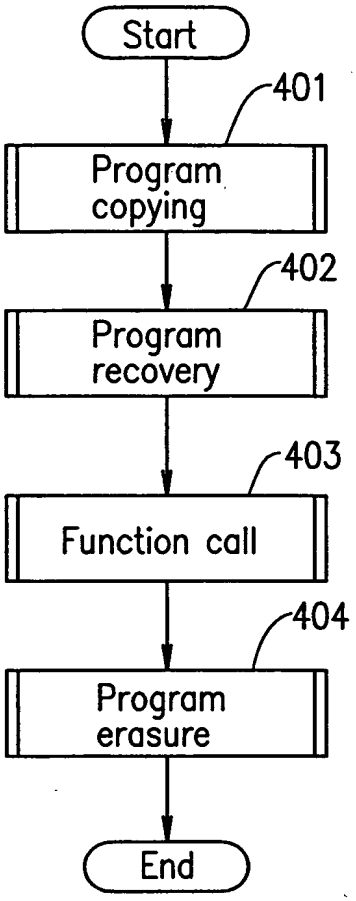

Start
401
Program copying
402
Program recovery
403
Function call
404
Program erasure
End

FIG. 5A

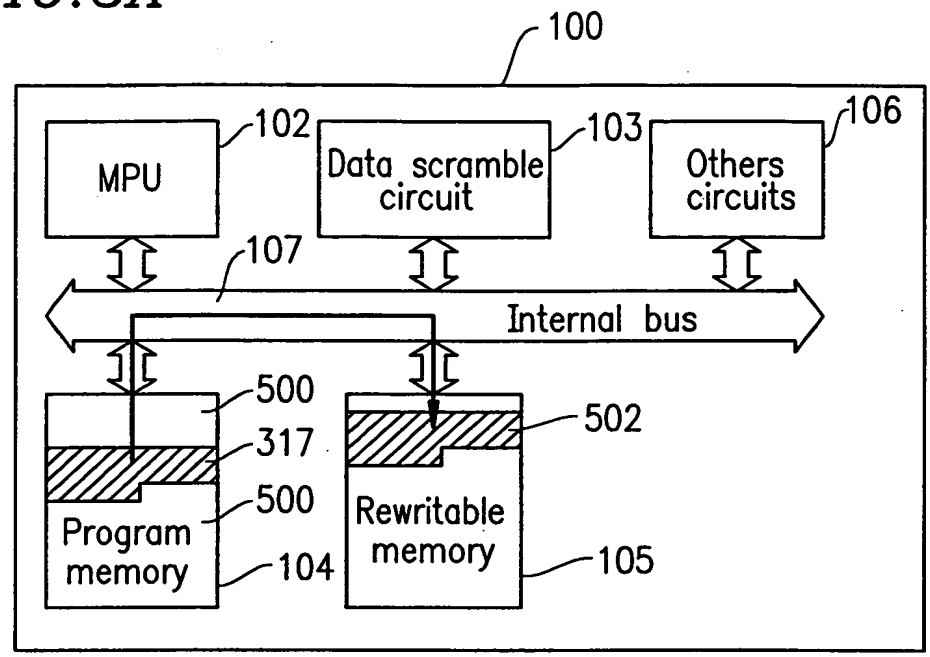

100
MPU
102
Data scramble circuit
103
Others circuits
106
107
Internal bus
500
317
500
Program memory
104
502
Rewritable memory
105

FIG. 5B

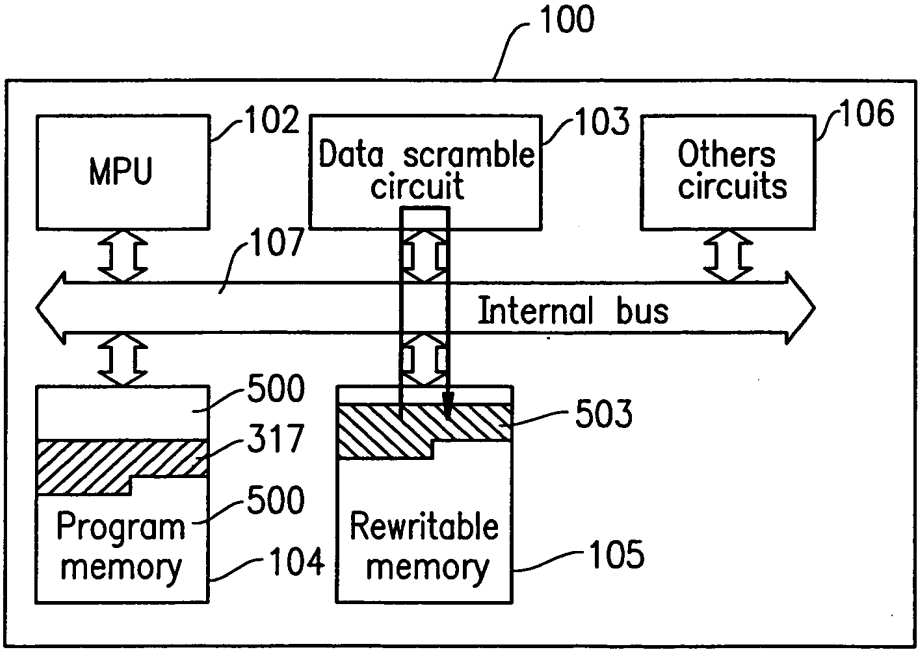

100
MPU
102
Data scramble circuit
103
Others circuits
106
107
Internal bus
500
317
500
Program memory
104
503
Rewritable memory
105

FIG. 5C

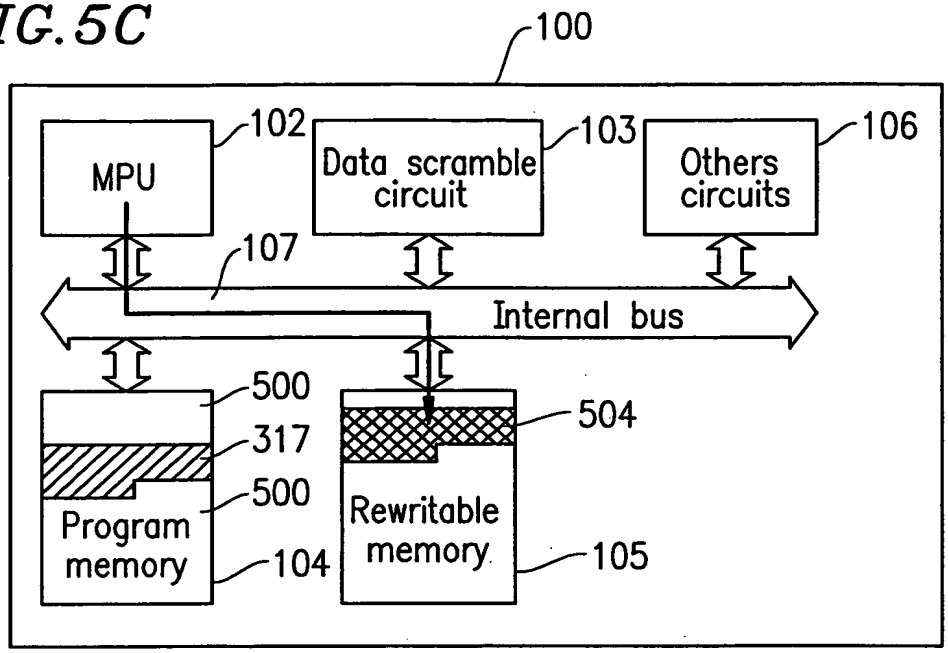

100
MPU
102
Data scramble circuit
103
Others circuits
106
107
Internal bus
500
317
500
Program memory
104
504
Rewritable memory
105

FIG. 6

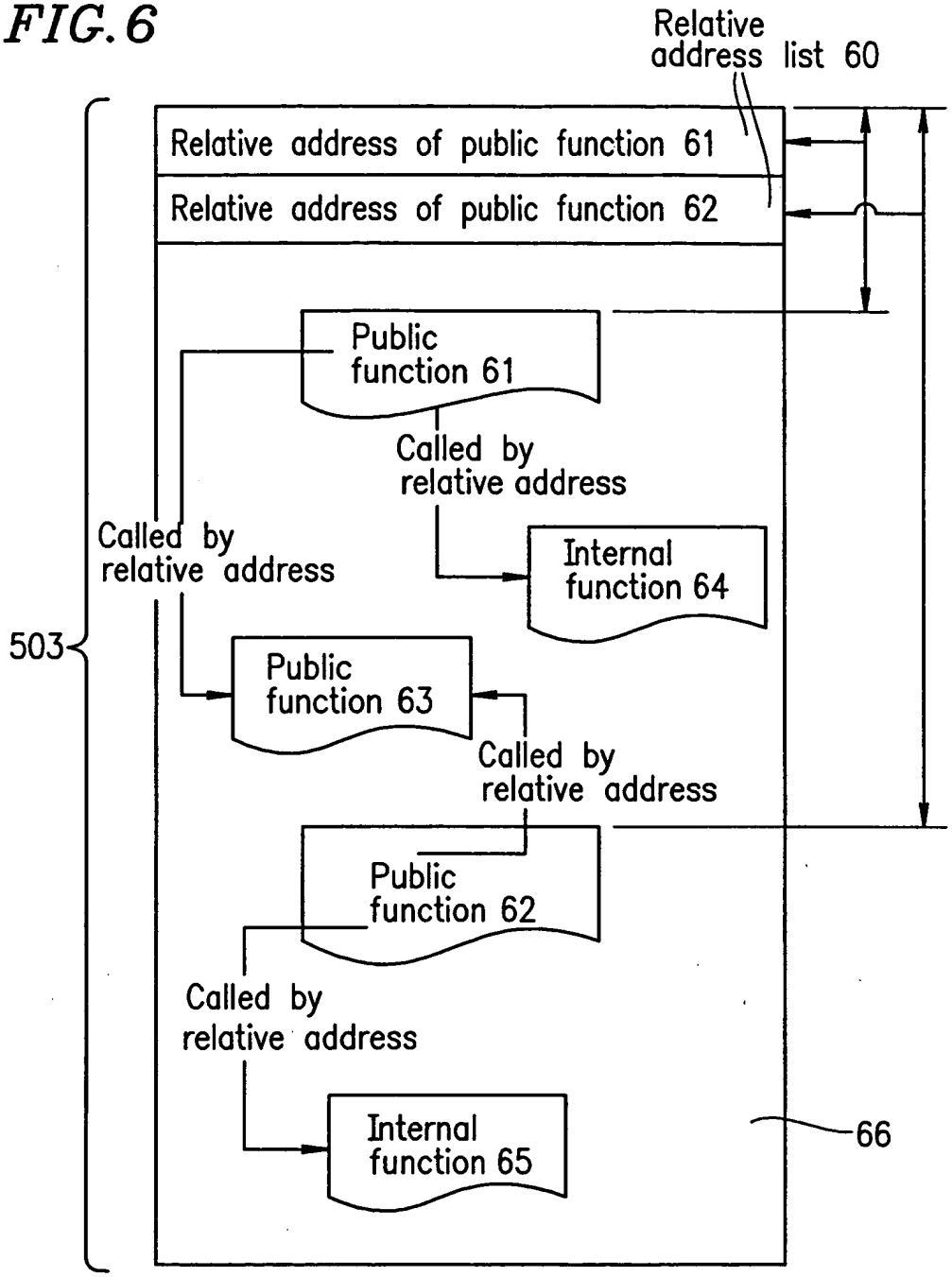
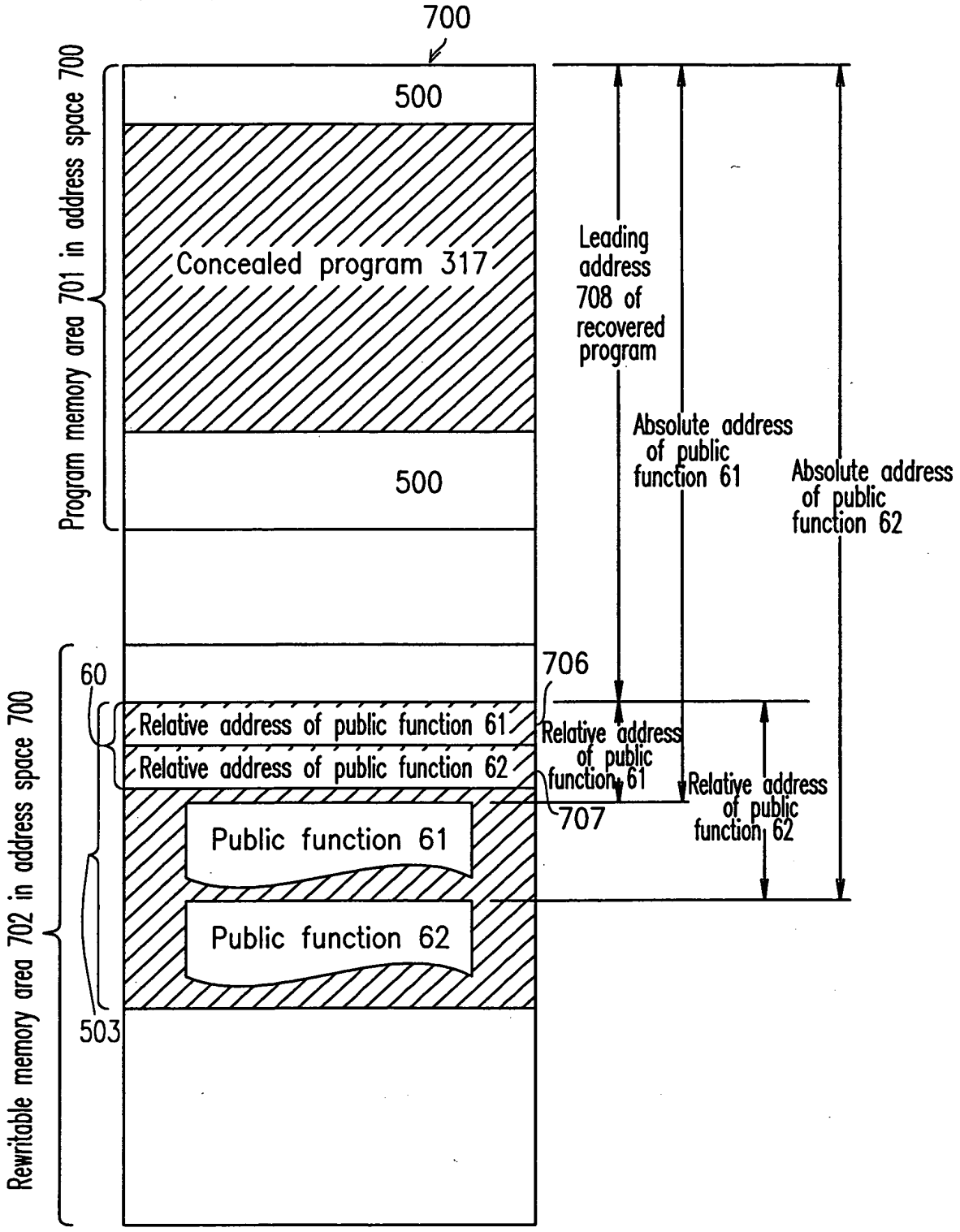

Relative address list 60
Relative address of public function 61
Relative address of public function 62
Public function 61
Called by relative address
Internal function 64
Called by relative address
503
Public function 63
Called by relative address
Public function 62
Called by relative address
Internal function 65
66

FIG. 7


700
500
Concealed program 317
500
Program memory area 701 in address space 700
Leading address 708 of recovered program
Absolute address of public function 61
Absolute address of public function 62
706
60
Relative address of public function 61
Relative address of public function 62
Relative address of public function 61
Relative address of public function 62
707
Public function 61
Public function 62
503
Rewritable memory area 702 in address space 700